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circuit should be designed to limit... current flow across conducting diode voltage across blocking diode Examples are shown to right... Figure 3.2: The two modes of operation of ideal diodes and the

## **Chapter #3: Diodes**

Where To Download Chapter 2 Microelectronic Circuits Sedra Smith 5th Edition forward bias condition. The positive causes the current flows through the diode in the forward direction. In forward bias condition the ideal diode acts as a short circuit.  
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The essence of transistor operation is that a change in  $v_{BE}$ ,  $v_{BE}$ , produces a change in  $i_C$ ,  $i_C$ . By keeping  $v_{BE}$  small,  $i_C$  is approximately linearly related to  $v_{BE}$ ,  $i_C = g_m v_{BE}$ , where  $g_m$  is known as the transistor transconductance. By passing  $i_C$  through  $R_C$ , an output voltage signal  $v_O$  is obtained. Use the expression

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### **Solutions-Problems-Chapter 6 (BJT) - Microelectronic ...**

For the circuit shown in Fig. P2.62, express  $v_O$  as a function of  $v_1$  and  $v_2$ . What is the input resistance seen by  $v_1$  alone? By  $v_2$  alone? By a source connected between the two input terminals? By a source connected to both input terminals simultaneously?

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